| Ref<br># | Hits | Search Query   | DBs   | Default<br>Operator | Plurals | Time Stamp       |
|----------|------|--|---|---------------------|---------|------------------|
| LII.     | 516  | (716/3):CCLS.  | US-PGPUB;<br>USPAT;<br>USOCR                                      | OR                  | OFF     | 2005/09/12 13:08 |
| L2       | 15   | preprocessor (\$2hdl or (hardware<br>near3 description) or specification)<br>control near3 file synthesi\$5<br>verilog | US-PGPUB;<br>USPAT;<br>USOCR                                      | AND                 | ON      | 2005/09/12 13:16 |
| L3       | 1    | (preprocessor (\$2hdl or (hardware near3 description) or specification) control near3 file synthesi\$5 verilog).clm.   | US-PGPUB;<br>USPAT;<br>USOCR                                      | AND                 | ON      | 2005/09/12 13:13 |
| L4       | 15   | preprocessor (\$2hdl or (hardware<br>near3 description) or specification)<br>control near3 file synthesi\$5<br>verilog | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND                 | ON      | 2005/09/12 13:16 |